

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	0	((clock adj1 source) and (first adj1 circuit) and (first adj1 voltage adj1 source) and (downstream adj1 latch)).clm.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/24 18:29
S2	0	((clock adj1 source) and ((first adj1 circuit) with (first adj1 voltage adj1 source)) and (downstream adj1 latch)).clm.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/24 18:31
S3	1	((clock adj1 source) and ((first adj1 circuit) with (first adj1 supply adj1 voltage)) and (downstream adj1 latch)).clm.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/24 18:31
S4	0	((microprocessor) with (first adj1 supply adj1 voltage) with (clock adj1 source)) and ((converter adj1 circuit) with (first adj1 level adj1 shifter))).clm.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/24 18:34
S5	0	((microprocessor) and (first adj1 supply adj1 voltage) with (clock adj1 source)) and ((converter adj1 circuit) with (first adj1 level adj1 shifter))).clm.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/24 18:34
S6	1	((microprocessor) with (first adj1 supply adj1 voltage)) and (clock adj1 source) and (i/o) and ((converter adj1 circuit) with (first adj1 level adj1 shifter))).clm.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/24 18:36
S7	10	((supply\$3 with first with voltage) with (second with voltage)) and (shift\$3 with first with data with signal) and (generat\$3 with level with shifted)).clm.	US-PGPUB; USPAT; EPO; IBM_TDB	OR	OFF	2007/04/24 18:38